ABSTRACT OF THE DISCLOSURE

A memory device controllable with user-defined commands includes a memory array accessible for reading and writing data therein, a command module that receives user-defined commands for controlling access to the memory array, and a mode module that stores command definitions respectively associated with the user-defined commands. Each command definition includes a memory access command, such as a read command or a write command, and at least one memory access parameter, such as the burst length setting and the read or write latency. By sending a program command to the command module, command definitions of the user-defined commands can be programmed into the mode module. Each memory array access commanded by a user-defined command is controlled in accordance with the memory access parameters in the command definition associated with the user-defined command.